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1220-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Benaissa, et al.

Docket No: TI-30681

Serial No: To be Assigned

Examiner: TBD

Conf. No: To be Assigned

Art Unit: TBD

Filed: 11/27/2001

For: SEMICONDUCTOR VARACTOR WITH REDUCED PARASITIC RESISTANCE

Assistant Commissioner for Patents
Washington, DC 20231

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I hereby certify that the above correspondence is being
deposited with the U.S. Postal Service with sufficient
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service under 37 CFR 1.10 and is addressed to: Assistant
Commissioner for Patents, U.S. Patent and Trademark
Office, P.O. Box 2327, Arlington, VA 22202, on
11-27-01

Ann Trent
Ann Trent

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above identified application, please amend the
specification by inserting before the first line the sentence:

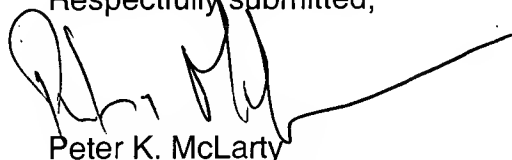
a' --This application claims priority under 35 USC § 119(e)(1) of provisional
application Serial No. 60/253,620, filed November 28, 2000.--

Should the Examiner have any comments or suggestions concerning this
application, it is respectfully requested that the Examiner contact the undersigned in
order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicants petition for an Extension of Time under 37
CFR 1.136. Please charge any fees in connection with the filing of this paper, including

extension of time fees, to the deposit account of Texas Instruments Incorporated,
Account No. 20-0668.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Peter K. McLarty', with a long horizontal line extending to the right.

Peter K. McLarty
Agent for Applicants
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20-0668